



Attorney's Docket No.: 10559-075002  
Intel Docket No.: P7567C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IFW

Applicant : Adiletta et al.

Art Unit : 2183

Serial No. : 10/615,500

Examiner : Ellis, Richard L

Filed : 7/8/2003

Assignee : Intel Corporation

Title : PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

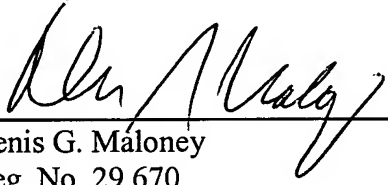
AMENDMENT IN REPLY TO ACTION OF SEPTEMBER 6, 2005

In response to the Notice of Non-Compliant Amendment dated September 6, 2005, enclosed are replacement pages for the Claims section for the Preliminary Amendment filed on June, 16 2005. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: \_\_\_\_\_

10/5/05

  
\_\_\_\_\_  
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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

October 5, 2005

Date of Deposit

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